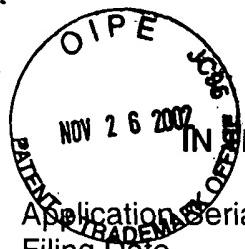


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/768,878
Filing Date January 23, 2001
Inventor Graham Wolstenholme
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Richard Booth
Attorney's Docket No. MI55-003
Title: Methods of Forming a Line of FLASH Memory Cells

PRELIMINARY AMENDMENT TO ACCOMPANY RCE FILING

To: Box RCE
Assistant Commissioner for Patents
Washington, D.C. 20231

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)
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Responsive to the Notice of Allowance dated October 30, 2002, Applicant
remarks as follows:

REMARKS

Claims 24-31, 36, 37, 39-41 and 45-54 are in the application.

A Notice of Allowance and Issue Fee Due statement was issued by the U.S. Patent and Trademark Office in this application on or about October 30, 2002. However, there remains prior art which was submitted by Applicant but not initialed by the Examiner. Such art was cited in a Form PTO-1449 which accompanied a Supplemental Information Disclosure Statement (SIDS) signed by the undersigned on September 30, 2002, and which was included with the filing of an RCE on that date.